Title: Method and System For Inspecting Electronic Components Mounted on Printed Circuit Boards
First Named Inventor: John J. Weisgerber, et al.

Application of Printed Circuit Boards
First Named Inventor: John J. Weisgerber, et al.

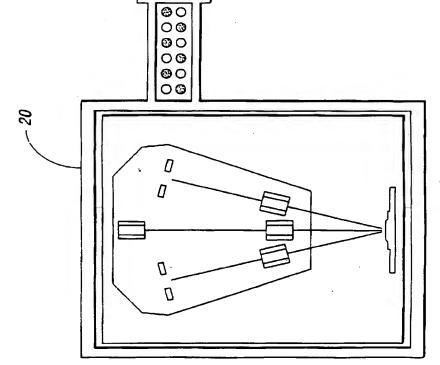
Application of Printed Circuit Boards
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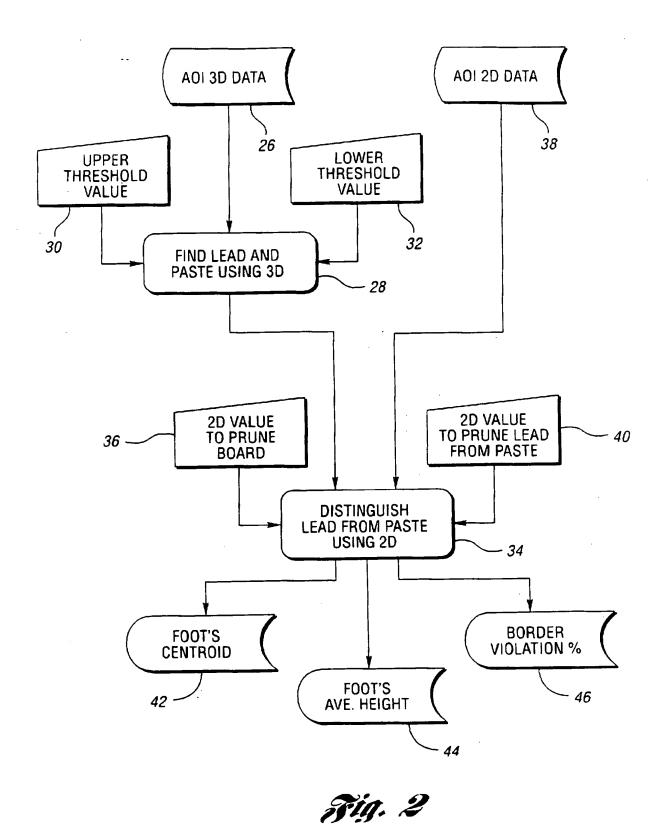
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Application Serial No.: / Atty. Docket No.: GSIL 01



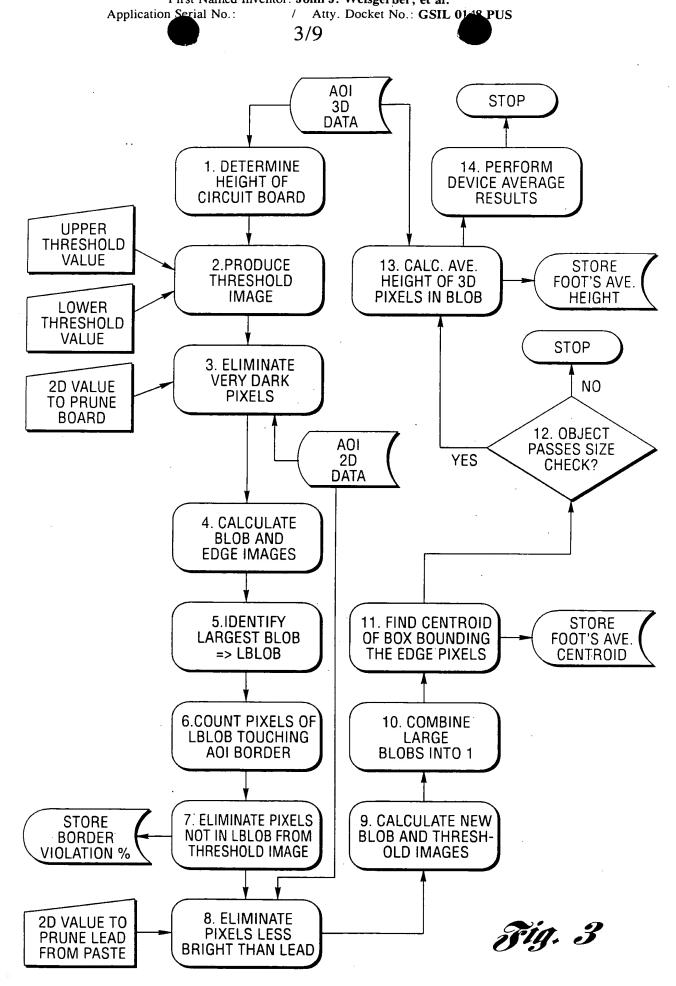
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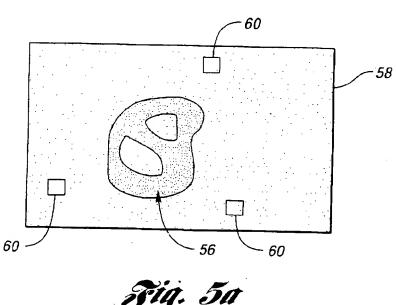
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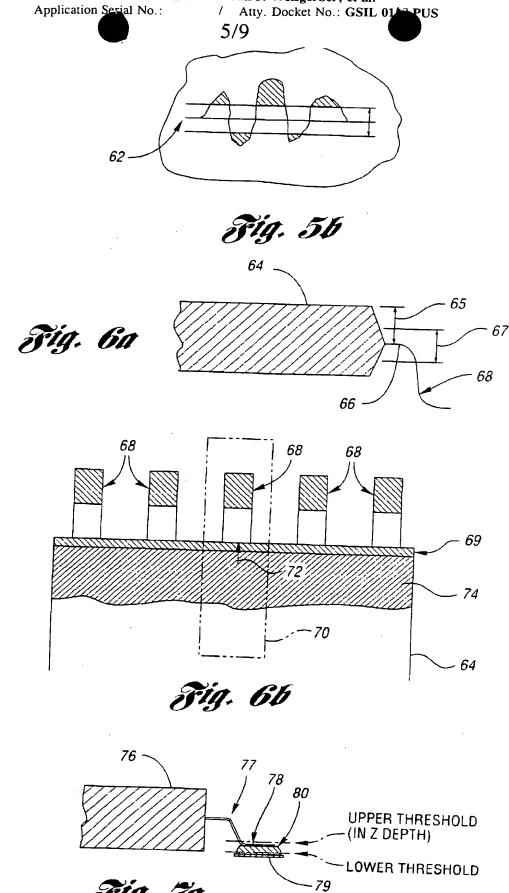
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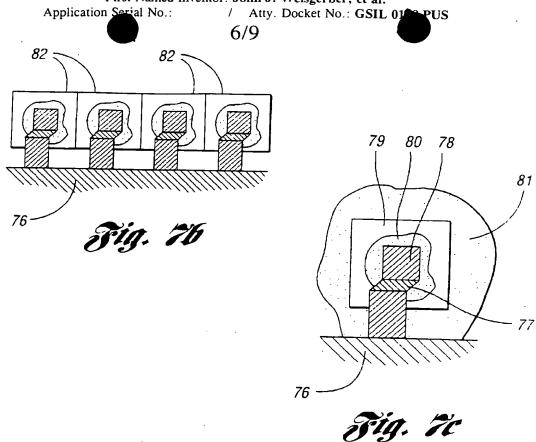
First Named Inventor: John J. Weisgerber, et al. / Atty. Docket No.: GSIL Que PUS Application Social No.: 4/9 -75% OF BOX OVER PART *50* -50 Fig. 4a 75% OF BOX OVER PART 52 *50* 50 HIGH THRESHOLD 52 -THESE DISTANCES ARE PARAMETERS LOW THRESHOLD -60 58

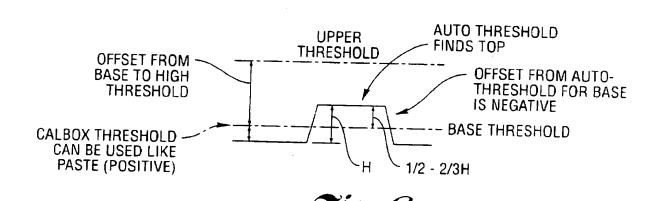












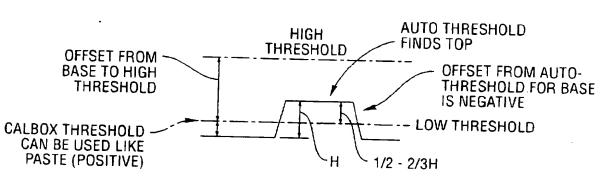
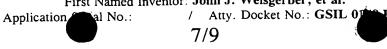


Fig. 9n



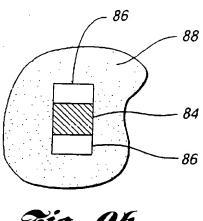
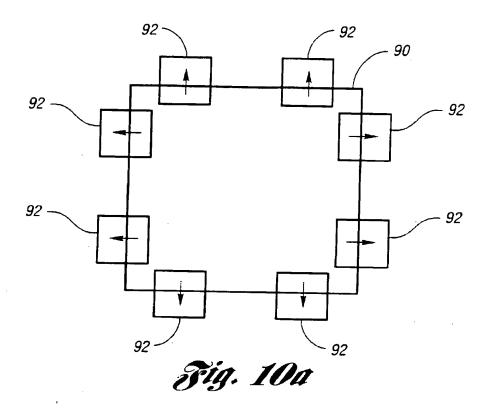


Fig. 96



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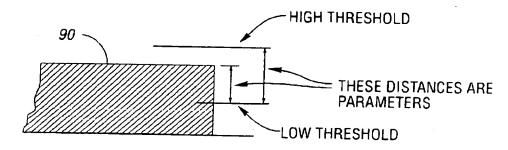


Fig. 106

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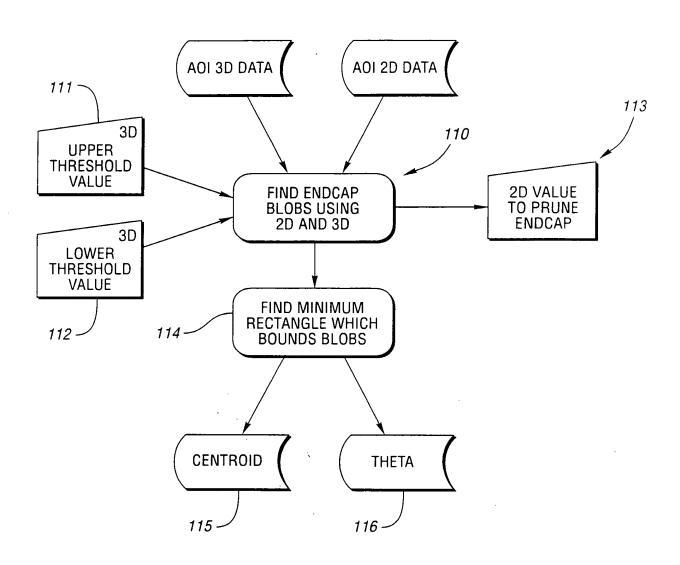
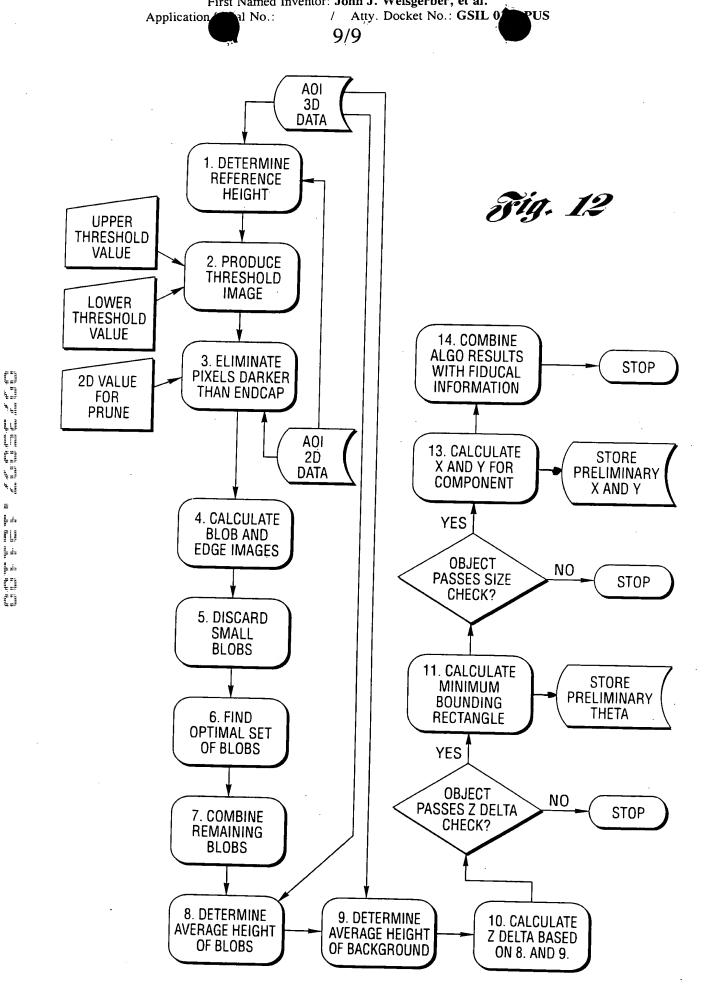


Fig. 11



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